

ACKNOWLEDGEMENT RECEIPT

Electronic Version 1.1

Stylesheet Version v1.1.1

**Title of
Invention**

CIRCUIT AND METHOD OF ESTABLISHING DC BIAS LEVELS IN
AN RF POWER AMPLIFIER

Submission Type : Information Disclosure
Statement

Application Number:

10/743221



EFS ID:

91583

Server Response:

Confirmation Code	Message
ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
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First Named Applicant: Alan Westwick

Attorney Docket Number: SIL.P0063

Timestamp: 2005-08-29 22:48:32 EDT

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File Listing:

Doc. Name	File Name	Size (Bytes)	Date Produced (yyyymmdd)
us-fee-sheet	P63-ids2-usfees.xml	1464	2005-08-29
us-fee-sheet	us-fee-sheet.xml	25930	2005-08-29
us-fee-sheet	us-fee-sheet.dtd	11968	2005-08-29
us-ids	P63-ids2-usidst.xml	2281	2005-08-29
us-ids	us-ids.dtd	7763	2005-08-29
us-ids	us-ids.xml	12026	2005-08-29
package-data	P63-ids2-pkda.xml	2235	2005-08-29
package-data	package-data.dtd	27025	2005-08-29
package-data	us-package-data.xml	19263	2005-08-29
Total files size		109955	

Message Digest: 9dc767733c94e1a5140a6c3bcee73ec8389ec2b4

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